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NoCs In Authoritative MPSoC Reference

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Communication between two remote blocks can go via several buses, and every section ... Where bus-based solutions reach their limit, packet-switched networks are poised to take over [3]. A packet ...

Future Trends in SoC Interconnect

As opposed to the prior solutions, each

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node that connects to this setup has the same topology, just one PCA9615 per drop. There's one exception, though. The final drop needs to have termination ...

Taking The Leap Off Board: An Introduction To I2C Over Long Wires
Part 1 contains the story in six chapters,

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each of which concludes with a problem set; Part 2 is more mathematical, and looks at the axiom systems, and gives solutions to the problems. The ...

Journey into Geometries

Q1 2021 Earnings Conference Call May
05, 2021 8:30 AM ET Company

Participants Yonah Lloyd - Vice President

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Investor Relations Yoav Zeif - Chief ...

Stratasys' (SSYS) CEO Yoav Zeif on Q1 2021 Results - Earnings Call Transcript

In most major universities one of the three or four basic first-year graduate mathematics courses is algebraic topology. This introductory text is

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suitable for use in a course on the subject or for ...

Algebraic Topology

It typically requires $1\frac{1}{4}$ to $1\frac{1}{5}$ the power of IEEE 802.11b solutions. Typical PDA implementations yield ... Bluetooth Technology Primer This section provides a basic review and update on Bluetooth

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Bluetooth and Beyond: Wireless Options for Medical Devices

Graphene oxide (GO) fiber was selected as a model because of the nature of GO sheets, including two-dimensional (2D) topology, abundant chemical moieties, super-flexibility, and self-adhesion ...

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Reversible fusion and fission of graphene oxide-based fibers

In these situations, the buck-boost topology shown in Figure 4 is the best solution. The input voltage and LED cathode are connected to the BSTIN/BKLED- terminal. 4. The LTM8042's connections ...

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This change from a parallel bus to serial links simplifies the topology a lot compared to ISA ... PCIe card edge connector cross section. An important distinction with PCIe is that there is ...

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The Bus That's Not A Bus: The Joys Of Hacking PCI Express

The architecture consists of three main stages: A power supply section implementing a QR fly-back topology based on the STD7N65M6 MDmesh™ M6 primary MOSFET, which combines a very low RDS(on) per area ...

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Compact 27W USB Type-C Power Delivery 3.0 with PPS adapter

Evaluate the most appropriate NoC topology, plug the IP cores on it ... dedicated tools have to be used. Numerous solutions are proposed in the literature. The Polaris framework [8] offers a complete ...

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SPIRIT IP-XACT Controlled ESL Design Tool Applied to a Network- on-Chip Platform

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Power Inductors Come In Small Sizes

Williams and her collaborator Yuji Kodama of the Ohio State University used the diagrams to study solutions to the so ... to the mathematical field of topology, which categorizes surfaces by ...

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The Art of Mathematics in Chalk

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