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developments have
made CMOS a strong
candidate in high-
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CMOS technology has
become the technology
of choice for the
integrated receiver
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performance is not as good as SiGe or GaAs. But most of the baseband (BB) ICs are implemented with CMOS. Thus, it gives the advantage to CMOS in applications where a single chip that combines RF and BB IC is desired.

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Ayman Hamouda A
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associated with the

VCO and the frequency
divider in the RF CMOS
phase-locked loop.

There are five

important contributions
in this research. Firstly,
a method for

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designers are required
to have in-depth
understanding on the

Yan~Lu Wing- Hung~Ki CMOS Integrated Circuit Design for ...

Abstract—This paper
describes precision
techniques for the
design of comparators
used in high-
performance analog-to-
dig-ital converters
employing parallel
conversion stages.

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Following a review of
conventional offset
cancellation
techniques, circuit
designs achieving 12-b
resolution in both
BiCMOS and CMOS 5-V
technologies are
presented.

Design techniques for high-speed, high- resolution comparators

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141-GHz amplifier was designed for the 40-nm CMOS process. The core was kept reasonably compact 201 x 284 μm^2 in spite of the five-stage differential amplifier by using a layout avoiding the coupling effect of adjacent shunt stub lines. The peak gain and 3-dB bandwidth were 20dB at 148 and 22 GHz, respectively.

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integrated circuits,
concentrating on
device, circuit, and

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Integrated circuit
design, or IC design, is
a subset of electronics

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engineering,
encompassing the
particular logic and
circuit design
techniques required to
design integrated
circuits, or ICs.ICs
consist of miniaturized
electronic components
built into an electrical
network on a
monolithic
semiconductor
substrate by
photolithography.. IC
design can be divided
into the broad

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Abstract. In this paper,
a review and analysis
of different design
techniques for (ultra)
low-voltage integrated
circuits (IC) are
performed. This

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analysis shows that the most suitable design methods for low-voltage analog IC design in a standard CMOS process include techniques using bulk-driven MOS transistors, dynamic threshold MOS transistors and MOS transistors operating in weak or ...

Design techniques for low-voltage analog integrated ...

During the last decade,

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CMOS has become increasingly attractive as a basic integrated circuit technology due to its low power (at moderate frequencies), good scalability, and rail-to-rail operation. T

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The 65nm Bulk CMOS prototype discussed in Chapter 4 also utilizes only standard MOS capacitors to regulate the output voltage at $\sim 1V$ from a $\sim 2.9V-4V$ input. ... %0 Thesis %A

Le, Hanh-Phuc %T
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